

SN 9/239,948

- 1 -

Docket: 0756-1930



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re DIVISIONAL Application of )  
Shunpei YAMAZAKI et al. )  
Based On Serial No. 08/847,314 ) Art Unit: 2811  
Which Was Filed: April 23, 1997 ) Examiner: G. Munson  
For: LASER PROCESSING METHOD, )  
METHOD FOR FORMING A FLASH )  
MEMORY, INSULATED GATE SEMI- )  
CONDUCTOR DEVICE AND METHOD )  
FOR FORMING THE SAME ) Date: January 29, 1999

INFORMATION DISCLOSURE STATEMENT

Honorable Assistant Commissioner for Patents  
Washington, D.C. 20231

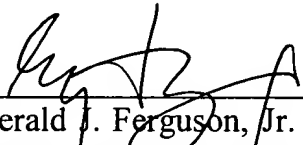
Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, it is requested that the references listed on the attached Form PTO-1449 be made of record in the above-identified application.

The references listed on the attached Form PTO-1449 were cited in parent application Serial No. 08/847,314 filed April 23, 1997, and the predecessor applications including Serial No. 08/692,227, filed August 7, 1996;

08/355,652, filed December 14, 1994 and 08/142,048, filed October 28, 1993.

Respectfully submitted,



---

Gerald J. Ferguson, Jr.  
Registration No. 23,016

Sixbey, Friedman, Leedom & Ferguson, P.C.  
8180 Greensboro Drive, Suite 800  
McLean, Virginia 22102  
(703) 790-9110